

IN THE SPECIFICATION:

Table 1 before line 5 of page 6 is amended to replace "Iih" with "Iil" in the third row as follows:

Table 1

A1

STATE	CURRENT	VOLTAGE
HIGH	$-20 \mu\text{A} < I_{ih} < 20 \mu\text{A}$	$2.2 \text{ V} < V_{ih} < 5.25\text{V}$
LOW	$-20 \mu\text{A} < \cancel{I_{ih}} \underline{I_{il}} < 20 \mu\text{A}$	$-0.5\text{V} < V_{il} < 0.7\text{V}$

Table 2 before line 9 of page 6 is amended as follows:

Table 2

A2

STATE	CURRENT	VOLTAGE
HIGH	$-20 \mu\text{A} < I_{ih} < 20 \mu\text{A}$	$2.2 \text{ V} < V_{ih} < 5.25\text{V}$
LOW	$0 \mu\text{A} < \underline{I_{il}} < 1 \text{ mA}$ $\cancel{I_{ih}} < -1 \text{ mA}$	$-0.5\text{V} < V_{il} < 0.7\text{V}$

Amend the paragraph beginning at line 9 of page 6 as follows:

A3

Figure 1 illustrates an isolation technique for the bussed signals. Instead of providing individual control signals to each drive, the signals are provided from a common bus to each of driver 110-130 through an isolation resistor, RD. In this embodiment a single isolation resistor, RD couples each drive to a stub coupled to the common bussed signal. Resistor 124 is a passive component. In an alternative embodiment, active circuitry component 111 is used for isolation. The illustrated bussed signal 100 is one of the SEL_N signal lines.

Amend the paragraph at lines 16-21 of page 7 as follows:

A4

Figure 2 illustrates a Thévenin equivalent model of the bussed signal to a single selected drive 210 for the HIGH state. The input HIGH state is

04
affected by shorted drives. Referring to Figures 1 and 2, VTH 230 and RTH 220 correspond to a Thévenin equivalent circuit for VCC, RA, and RF. VTH corresponds to the voltage at node 150. In the failed HIGH state, the values of RTH and VTH are determined as follows:

Amend the character reference numbers at lines 10-13 of page 10 as follows:

05
Figure 5 illustrates a circuit model of the signal path from the bus driver to a single drive with a pull up resistor for the bus HIGH state. Referring to Figures 4-5, VTH ~~520~~ 530 and RTH ~~530~~ 520 form a Thévenin equivalent circuit for VCC, RA, and RF. Vin for the drive is calculated as
